

501.37426CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: I. FUJII et al.  
Serial No.: 09/928,497  
Filed: August 14, 2001  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
Group: 2827  
Examiner: L. CRUZ

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**SUBMISSION OF (Corrected) FORMAL DRAWINGS**

**ATTN: BOX AF**

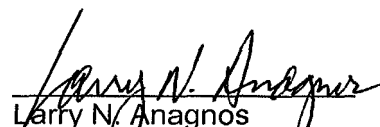
Commissioner for Patents  
Washington, D. C. 20231

March 18, 2003

Sir:

In response to the Final Office Action, dated December 18, 2002,  
submitted herewith are twelve (12) sheets of formal (corrected) drawings  
illustrating Figs. 1-8, 9(A)-9(B) and 10-12 in connection with the above-identified  
application.

Respectfully submitted,  
**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

  
Larry N. Anagnos  
Registration No. 32,392

LNA:dks  
Attachments  
703-312-6600